

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) A reproduced signal processing apparatus, ~~being an apparatus for processing reproduced data reproduced from a recording medium in which data is recorded in sync blocks units together with the identification information of sync block, reproduced at an arbitrary speed of $\pm \alpha$ times (α being an integer) of usual~~ a reproducing speed, comprising:

sync block detecting means for detecting ~~the~~ identification information associated with ~~of the~~ reproduced data,

data information generating means for generating data information comprising ~~composed of~~ track information, field information and frame information from the identification information,

first memory means for storing plural frames of the reproduced data,

memory writing means for writing reproduced data in said first memory means on the basis of the identification information,

memory reading means for reading out ~~parallel the~~ data of n frames in parallel (n being an integer of 2 or more satisfying the relation of $\alpha \leq n$) accumulated in said first memory means, and

transmitting means for transmitting n ~~pieces frames~~ of transmission data in parallel by restructuring ~~or without restructuring n pieces of frame data being read out by said memory reading means the data of n frames into a main data output and a sub data output~~ on the basis of said ~~the~~ data information.

2. (Currently Amended) The reproduced signal processing apparatus of claim 1, wherein the main data output of said transmitting means either transmits n pieces

~~frames of frame-the data being read out by said memory reading means to said main data without restructuring, or transmits n pieces-frames of transmission data to said main data~~ restructured by selecting m ~~pieces-frames~~ out of n ~~pieces-frames~~ (m being an integer satisfying the relation of $m < n$), according to the data information.

3. (Currently Amended) The reproduced signal processing apparatus of claim 2, wherein said transmitting means, ~~transmitting~~ transmits one of n ~~pieces-frames~~ of transmission data as said main data and ~~others-a remainder as said sub data, and~~ restructures the transmission data so as to transmit all frames of data reproduced at α times by transmitting all of said main data and said sub data,

whereby a substantial number of frames can transmit many frame data of data reproduced at α times is transmitted as said main data, and also restructures so as to transmit all frame data reproduced at α times by transmitting all of main data and sub data.

4. (Currently Amended) The reproduced signal processing apparatus of claim 2, wherein said transmitting means transmits ~~the information showing whether n pieces of frames of data to be transmitted are valid or invalid, by adding~~ said information to the transmission data.

5. (Currently Amended) The reproduced signal processing apparatus of claim 1, wherein said transmitting means further comprises second memory means for accumulating the data of n pieces-of-frames data being read out in parallel by said memory reading means, each data of said n frames are delayed by one frame and two frames by said second memory means, for the portion of three frames, and

m piecesframes (m being 1 or 2) of ~~frame data~~ are read out from the second memory means on the basis of the data information, ~~from the data delayed by one frame and two frames, by controlling said second memory means, and are~~ restructured into two parallel outputs ~~pieces of transmission data.~~

6. (Currently Amended) The reproduced signal processing apparatus of claim 5, wherein said transmitting means restructures the data delayed by one frame and two frames so as to obtain a reproduction output of α times at the a reception side.

7. (Currently Amended) The reproduced signal processing apparatus of claim 5, wherein said transmitting means transmits ~~the information~~ showing whether two piecesparallel outputs of frame data to be transmitted are valid or invalid, by adding said information to the data.

8. (Currently Amended) A reproduced signal processing apparatus, ~~being an apparatus for processing reproduced data reproduced from a recording medium in which data is recorded in sync blocks units together with the identification information of sync block,~~ reproduced at an arbitrary speed of $\pm \alpha$ times (~~α being an integer~~) of ~~usual~~ a reproducing speed, comprising:

sync block detecting means for detecting ~~the~~ identification information associated with ~~of~~ the reproduced data,

data information generating means for generating data information comprising ~~composed of~~ track information, field information and frame information from the identification information,

first memory means for storing plural frames of the reproduced data,

memory writing means for writing the reproduced data in said first memory means on the basis of the identification information,

memory reading means for reading out ~~parallel~~ the data of n frames in parallel (n being an integer of 2 or more satisfying the relation of $\alpha \leq n$) accumulated in said first memory means,

delay means for issuing ~~n piecesframes~~ of frame data being read out by said memory reading means by delaying one parallel output of said memory reading means by each of one field and two fields each, and ~~also issuing data of n-th frame~~

~~by delaying by three fields~~ delaying a second parallel output by each of one field, two fields and three fields, and

reproduction output control means for selecting and issuing outputs of said delay means and memory reading means in field units on the basis of the data information.

9. (Currently Amended) A reproduced signal processing apparatus, ~~being an apparatus for processing reproduced data reproduced from a recording medium in which data is recorded in sync blocks units together with the identification information of sync block, reproduced at an arbitrary speed of $\pm \alpha$ times (α being an integer) of usual a reproducing speed, comprising:~~

sync block detecting means for detecting ~~the~~ identification information associated with ~~of~~ the reproduced data,

data information generating means for generating data information comprising ~~composed of~~ track information, field information and frame information from the identification information,

first memory means for storing plural frames of the reproduced data,

memory writing means for writing the reproduced data in said first memory means on the basis of the identification information,

memory reading means for reading out ~~parallel~~ the data of n frames in parallel (n being an integer of 2 or more satisfying the relation of $\alpha \leq n$) accumulated in said first memory means,

second memory means for accumulating the data of n frames ~~n pieces of frame data being~~ read out by said memory reading means for the portion of three frames each, and

reproduction output control means for selecting and issuing field data on the basis of the data information, from the data delayed by one frame and two frames by controlling said second memory means.

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10. (Currently Amended) The reproduced signal processing apparatus of claim 3, wherein said transmitting means transmits ~~the~~ information showing whether ~~n pieces of frames of~~ data to be transmitted are valid or invalid, by adding said information to the transmission data.

11. (Currently Amended) The reproduced signal processing apparatus of claim 6, wherein said transmitting means transmits ~~the~~ information showing whether two ~~piecesframes of-frame~~ data to be transmitted are valid or invalid, by adding said information to the data.